



# DDR SDRAM SO-DIMM MODULE, 2.5V

## 512MByte - 64MX64

### AVK6464U52C5266K2

### FEATURES

JEDEC Compliant DDR 266MHz PC2100 Version 1.0

- Clock frequency: 166MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 1.25 inch height

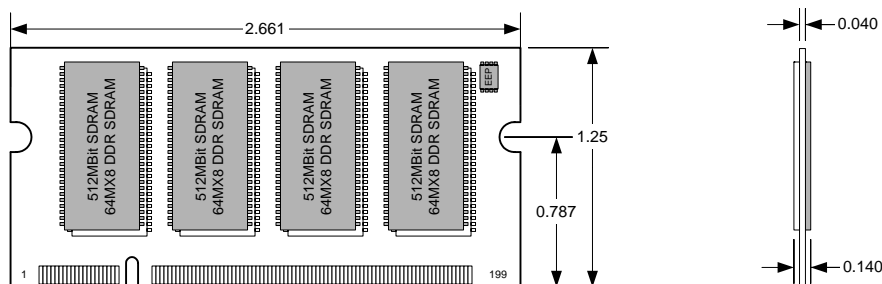
### DESCRIPTION

The AVK6464U52C5266K2 family consists of Unbuffered DDR SDRAM SODIMM memory module. This module is JEDEC-standard 200-pin, small-outline, dual in-line memory module. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVK6464U52C5266K2 memory module is 512MByte and organized as a 64MX64 array using (8) 64MX8 (4 internal banks) SDRAMs in TSSOP II packages.

All memory modules are fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

### PHYSICAL DIMENSIONS



All gray ICs are on the front side of the module, and all white ICs are on the back side of the module.

TSOP Package

**Avant Ordering Guides**

<b>AV</b>	<b>K</b>	<b>64</b>	<b>64</b>	<b>U</b>	<b>52</b>	<b>C</b>	<b>5</b>	<b>266</b>	<b>K</b>	<b>2</b>
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	K = 200-PIN SO-DIMM	64=X64	64=64M	U=UNBUFFERED	52=16Mx8x4	C = 2.5V	5=CAS LATENCY 2.5	266MHZ	K=DDR SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.



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