



DDR SDRAM SO-DIMM MODULE, 2.5V

256MByte - 32MX64

AVK6432U51C3400K1

FEATURES

JEDEC DDR 400MHz PC3200 Version 1.0

- Clock frequency: 200MHz with CAS latency 3
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 3
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Single-sided module
- 31.75mm (1.25 inch) height

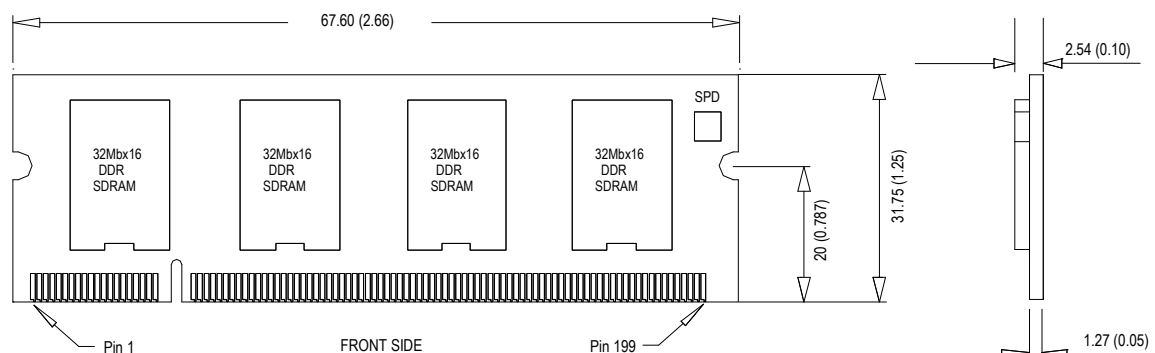
DESCRIPTION

The AVK6432U51C3400K1 family consists of Unbuffered DDR SDRAM SODIMM memory module. This module is JEDEC-standard 200-pin, small-outline, dual in-line memory module. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVK6432U51C3400K1 memory module is 256MByte and organized as a 32MX64 array using (4) 32MX16 SDRAMs in TSSOP II packages.

All memory modules are fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

Avant Ordering Guides

| AV | K | 64 | 32 | U | 51 | C | 3 | 400 | K | 1 |
|------------|---------------------|-----------|-----------|--------------|--------------|----------|-----------------|------------|-------------|----------|
| INVENTORY | MOD. TYPE | ORG. | DENSITY | PARITY | TYPE | VOLT. | FEATURE | SPEED | MODE | REV |
| AV = AVANT | K = 200-PIN SO-DIMM | 64=X64 | 32=32M | U=UNBUFFERED | 51 = 8Mx16x4 | C = 2.5V | 3=CAS LATENCY 3 | 400MHZ | K=DDR SDRAM | REV=1 |

Other options may be available. Call for specific part number information on options not listed.



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