



DDR SDRAM SO-DIMM MODULE, 2.5V 1GByte - 128MX64 AVK6428U61C5333K2-AP

FEATURES

JEDEC Standard DDR 333MHz PC2700 Version 1.0, Lead Free, RoHS compliant

- Clock frequency: 166MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 31.75mm (1.25 inch) height

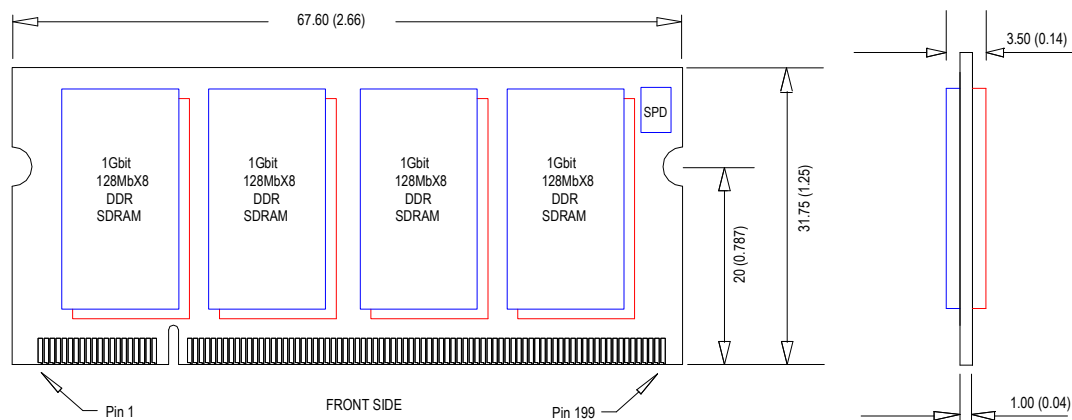
DESCRIPTION

The AVK6428U61C5333K2-AP is an Unbuffered DDR SDRAM SODIMM memory module. This module is JEDEC-standard 200-pin, small-outline, dual in-line memory module. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVK6428U61C5333K2-AP memory module is 1GByte and organized as a 128MX64 array using (8) 128MX8 DDR SDRAMs in lead free TSSOP II packages.

All memory modules are fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- All blue ICs are on the front, and all red ICs are on the back side of the module.
3- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

Avant Ordering Guides

AV	K	64	28	U	61	C	5	333	K	2
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	K = 200-PIN SO-DIMM	64 = X64	28 = 128M	U=UNBUFFERED	61= 16MX8x8	C = 2.5V	5 = CAS LATENCY 2.5	333MHz	K = DDR	REV=2

Other options may be available. Call for specific part number information on options not listed.



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